

ABSTRACT OF THE DISCLOSURE

An array substrate for a liquid crystal display, which includes a data line in a first direction; a gate line in a second direction perpendicular to the data line, wherein the data and gate lines cross to each other to define a pixel region; a thin film transistor disposed near a crossing of the data and gate lines, the thin film transistor comprising source, gate and drain electrodes; a black matrix over the thin film transistor and on the data line, the black matrix exposing a portion of the drain electrode; a color filter disposed within the pixel region, the color filter covering a portion of the drain electrode with exposing another portion of the drain electrode; and a pixel electrode on the color filter within a pixel region, the pixel electrode contacting an exposed portion of the drain electrode.